# ADVANCED LEVEL NATIONAL EXAMINATIONS, 2014 TECHNICAL AND PROFESSIONAL TRADES 

## EXAM TITLE: General Electronics

## OPTIONS: - Computer Electronics (CEL) <br> - Electronics and Telecommunication (ETL)

## DURATION: 3hours

## INSTRUCTIONS:

The paper is composed of three (3) Sections :

| Section I: Thirteen (13) questions, all Compulsory. | 55marks |
| :--- | :--- |
| Section II: Five (5) questions, Choose any Three (3). | 30marks |
| Section III: Three (3) questions, Choose any One (1). | $\mathbf{1 5 m a r k s}$ |

## SECTION I. THIRTEEN (13) COMPULSORY QUESTIONS.

1. What is the behavior of a Common Emitter amplifier when you remove bypass capacitor across the emitter-leg resistor?
2. A Field effect transistor operates with a drain current of 100 mA and a gate -source bias of -1 V . If the device has a forward transfer conductance in common source mode of 0.25 S , determine the change in drain current (in mA ) if the bias voltage increases to -1.1 V .

5marks
03. Classify IC's on the basis of their chip size.

4marks
04. From a bridge circuit below, using schematics, show how to get Thevenin equivalent of the circuit facing the resistance $R_{5}$.

05. Identify five (5) advantages of FETs over BJTs.

5marks
06. Draw the block diagram of a typical operational amplifier by specifying its main functions.

7marks
07. Using a block diagram show the structure of a sequential circuit and identify different possible states.

5marks
08. For the circuit shown below, find the value of RL for maximum power transfer.

5marks

09. Differentiate the oscillator from amplifier.

5marks
10. Calculate the resonant frequency of a Wien Bridge oscillator when $\mathrm{R}=17 \mathrm{k} \Omega$ and $\mathrm{C}=3200 \mathrm{pF}$.

3marks
11. Design RC elements of a Wien Bridge oscillator), for operation at 4.5 kHz .
12. Consider the circuit bellow and determine the set of capacity by a single capacity.

13. What is the avalanche breakdown?

2marks

## SECTION II. ATTEMPT ANY THREE (3) QUESTIONS.

14. For the series regulator given below:
$\mathrm{V}_{\mathrm{in}}=15 \mathrm{~V}, \mathrm{R}=200 \Omega$, the transistor gain $\beta=50$
$\mathrm{R}_{\mathrm{L}}=1.2 \mathrm{~K} \Omega, \mathrm{~V}_{\mathrm{Z}}=10 \mathrm{~V}$ and $\mathrm{V}_{\mathrm{BE}}=0.4 \mathrm{~V}$
Determine :
a) output voltage
b) load current
d) Zener current.


10marks
15. a) Consider the following circuit and determine output $Q$ if the conditions on $R, C P$ and $S$ are the following and $Q$ is initially at 1 .

## 8marks

CP: 0111000111000111000
R: 0010010000010010000
S: 0000100010000000010

b) Why is Hysteresis desirable in Schmitt-trigger?

2marks
16. Identify the component represented by each of the following symbols and describe its function using a truth table.
(A)

(B)

17. a) For the bridge network shown in Figure below determine the currents in each of the resistors.

b) Prove that for a class- B amplifier the overall efficiency is equal to $78.5 \%$

2marks

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